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CONFIRMATION NO. 6797

Bib Data Sheet

SERIAL NUMBER 10/624,398	FILING DATE 07/22/2003	CLASS <i>438</i> <i>716 / 11</i>	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. SC12862TC
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APPLICANTS

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** CONTINUING DATA *N...VS.....*** FOREIGN APPLICATIONS *N...VS.....*

IF REQUIRED, FOREIGN FILING LICENSE

GRANTED

** 03/25/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>✓</i> <i>John S.</i> Examiner's Signature	<i>VS</i> Initials	TX	5	20

ADDRESS

23125

FREESCALE SEMICONDUCTOR, INC.

LAW DEPARTMENT

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AUSTIN , TX

78729

TITLE

Method for converting a planar transistor design to a vertical double gate transistor design

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
RECEIVED 750		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____